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EV	5	2	1	0	4	3	7	05/11/93	Sawada et al.		257	392	APPROPRIA 192 08/10/92	
EL	6	1	9	8	1	4	0	03/06/01	Muramoto	o et al. 257 392		392	09/08/99	
EU	5	5	8	1	1	0	7	12/03/96	Kawamura et al.		257	392	12/14/94	
EL	5	0	1	6	0	7	7	05/14/91	Sato et al.		357	41	08/22/8	6
EL	6	4	7	6	4	3	0	11/05/02	Schmitz et al.		257	274	09/07/00	0
· el	4	4	7	2	8	7	1	09/25/84	Green et al.		29	571		
ev	6	0	2	7	9	7	8	02/22/00	Gardner et al.		438	289	01/28/97	
Ev	4	3	2	9	1	8	6	05/11/82	Kotecha et	al.	148	1.5	12/20/79	
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el	6	0	4	0	6	1	0	03/21/00	Noguchi et al. Klein		257 357	392	04/08/98	
el	4	5	7	5 8	6	9	4	05/27/80	Ariizumi et	al.	357	41	09/30/82	
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66	Richard S. Muller and Theodore I. Kamins, "Device Electronics for Integrated Circuits", Second Edition, pps. 477-479.
 	
EXAMINER	9 DATE CONSIDERED 9/1/04
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